



# SOT8027-1

wafer level chip-scale package; 9 bumps; 1.06 mm x 1.06 mm  
x 0.43 mm body

27 September 2022

Package information

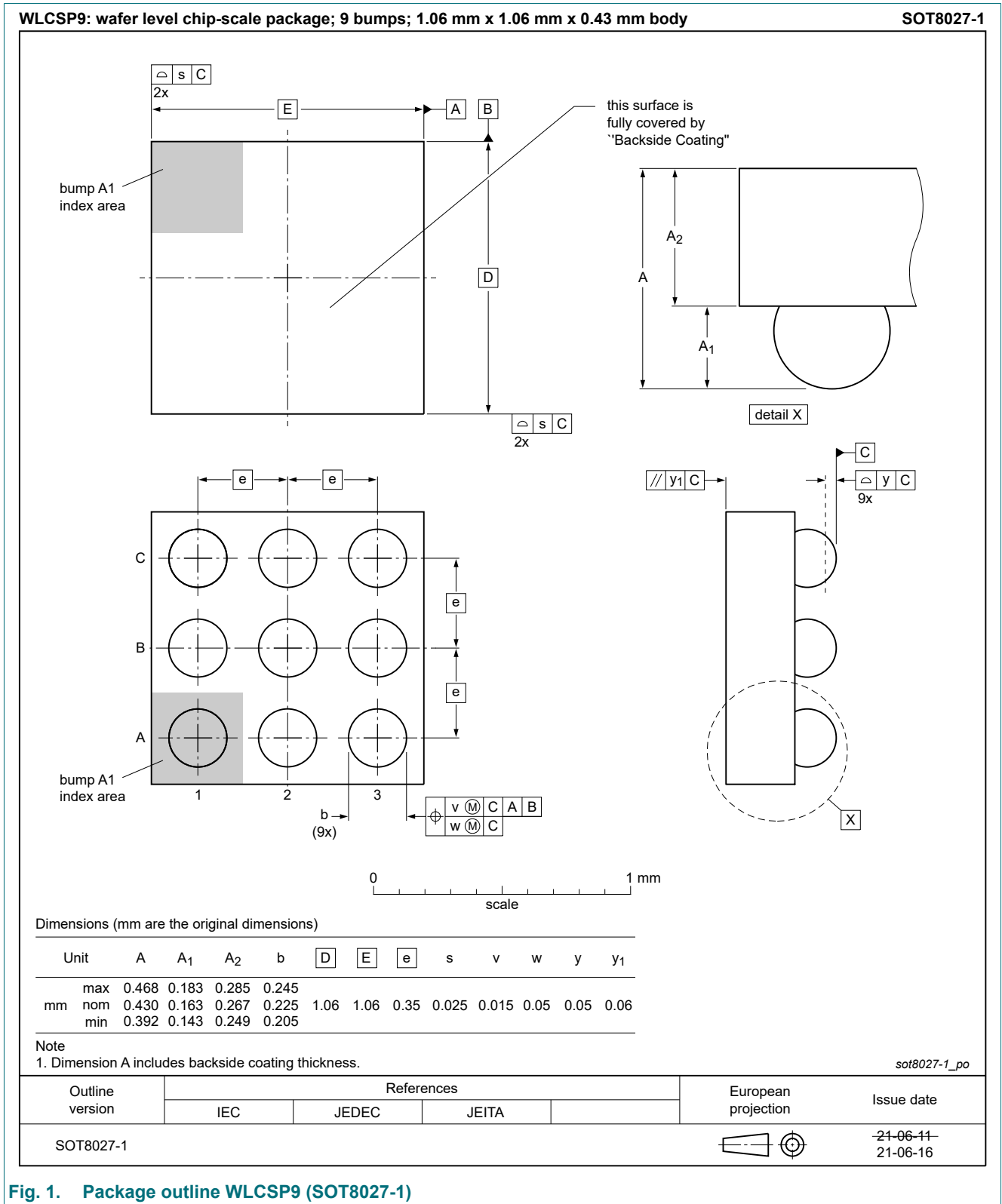
## 1. Package summary

<b>Terminal position code</b>	B (bottom)
<b>Package type descriptive code</b>	WLCSP9
<b>Package type industry code</b>	WLCSP9
<b>Package style descriptive code</b>	WLCSP (wafer level chip-size package)
<b>Package body material type</b>	X (other)
<b>Mounting method type</b>	S (surface mount)
<b>Number of package outline detail graphic references</b>	0
<b>Issue date</b>	16-6-2021
<b>Manufacturer package code</b>	SOT8027
<b>Footprint dimensions (mm)</b>	1.36 x 1.36
<b>Footprint area (mm<sup>2</sup>)</b>	1.85

Table 1. Package summary

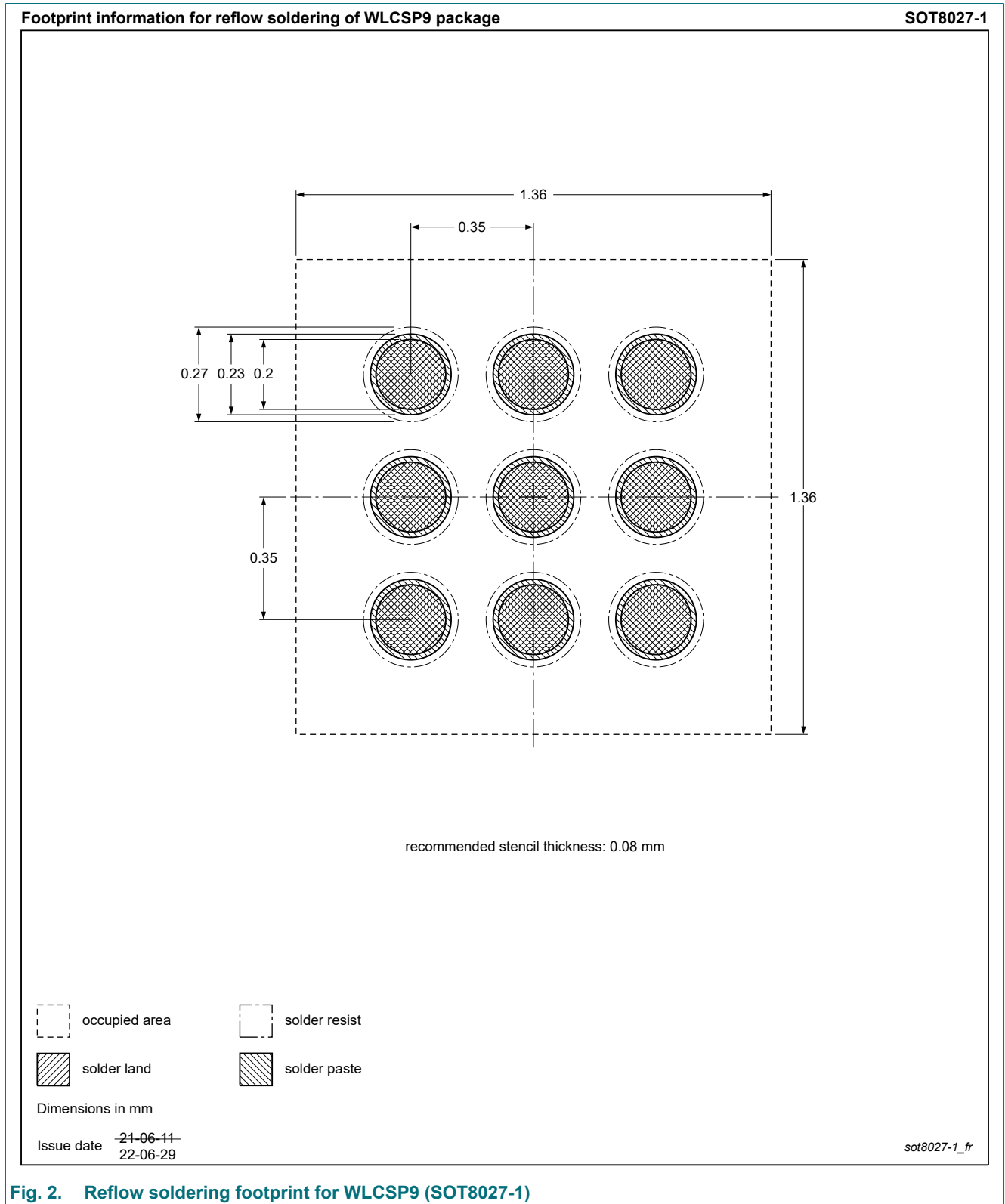
Symbol	Parameter	Min	Typ	Nom	Max	Unit
D	package length	-	-	1.06	-	mm
E	package width	-	-	1.06	-	mm
A	seated height	0.392	-	0.43	0.468	mm
A <sub>2</sub>	package height	0.249	-	0.267	0.285	mm
e	nominal pitch	-	-	0.35	-	mm
n <sub>2</sub>	actual quantity of termination	-	-	9	-	

## 2. Package outline



**Fig. 1. Package outline WLCSP9 (SOT8027-1)**

### 3. Soldering



**Fig. 2. Reflow soldering footprint for WLCSP9 (SOT8027-1)**

## 4. Legal information

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